



PRODUCT/PROCESS CHANGE NOTIFICATION

PCN MMS-SNV/07/2537
Notification Date 05/21/2007

tW at 5ms on 128Kbit, 64Kbit & 32Kbit serial I²C bus EEPROM 1.8V

SNV - MEMORY

Table 1. Change Identification

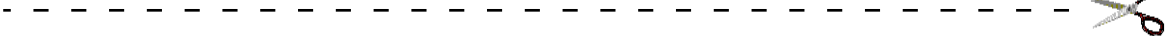
Product Identification (Product Family/Commercial Product)	M24128-R, M24C64-R, M24C32-R series
Type of change	Product electrical spec. change
Reason for change	Improve product feature
Description of the change	Write time max changes from 10ms to 5ms
Product Line(s) and/or Part Number(s)	See attached
Description of the Qualification Plan	See attached
Change Product Identification	Change effective from Date code 7 27 onwards
Manufacturing Location(s)	

Table 2. Change Implementation Schedule

Forecasted implementation date for change	02-Jul-2007
Forecasted availability date of samples for customer	02-Jul-2007
Forecasted date for STMicroelectronics change Qualification Plan results availability	02-Jul-2007
Estimated date of changed product first shipment	31-Jul-2007

Table 3. List of Attachments

Customer Part numbers list	
Qualification Plan results	



Customer Acknowledgement of Receipt		PCN MMS-SNV/07/2537	
Please sign and return to STMicroelectronics Sales Office		Notification Date 05/21/2007	
<input type="checkbox"/> Qualification Plan Denied	Name:		
<input type="checkbox"/> Qualification Plan Approved	Title:		
	Company:		
<input type="checkbox"/> Change Denied	Date:		
<input type="checkbox"/> Change Approved	Signature:		
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DOCUMENT APPROVAL

Name	Function
Poli, Christian	Division Marketing Manager
Rodrigues, Benoit	Division Product Manager
Yackowlew, Nicolas	Division Q.A. Manager



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tW at 5ms on 128Kbit, 64Kbit & 32Kbit serial I²C bus EEPROM 1.8V

What is the change?

The maximum Write Time (tW) of the 128Kbit, 64Kbit and 32Kbit serial I²C bus EEPROM with 1.8V to 5.5V operating voltage, will be modified in order to show a lower maximum value. The current value tW max at 10ms will be lowered at 5ms. The datasheet will be modified accordingly.

There are no die changes or process changes. The lower maximum Write Time value is granted by the new test program implementation.

Why?

The strategy of the STMicroelectronics Memory division is to support the growth of our customers on a long-term basis. In line with this commitment, the 128Kbit, 64Kbit and 32Kbit serial I²C bus EEPROM, 1.8V to 5.5V operating voltage with a lower tW max at 5ms, will enlarge the product performances and consequently improve the service to our customers.

When?

The production of the 128Kbit, 64Kbit and 32Kbit serial I²C bus EEPROM 1.8V with a lower tW max at 5ms will ramp up from Week 27 / 2007 and shipments will start from end of July 2007 onward.

How will the change be qualified?

The qualification will be done through validation tests, the Validation Report R0010307 is available.

How can the change be seen?

From the cut off **date code 727** (Week 27, 2007), all shipments will see the new maximum write time at 5ms.

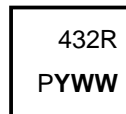
On the DEVICE MARKING, the **date code** is appearing coded by “**YWW**” identifiers, corresponding to **Year & Work Week** of assembly.

→ Example for M24C32-R (32Kb, 1.8V to 5.5V Vcc range)

TSSOP8
M24C32-RDW6TP



MLP 2X3
M24C32-RMB6TG



SO8N
M24C32-RMN6TP



The traceability for each device is as follows:

P = Assembly country and plant
Y = Last digit of the Year of Assembly: 7 for Year 2007
WW = Assembly Week code: starting from 27 for Week 27
T = Process Technology

Appendix 1 - Product Change Information

Product family / Commercial products:	M24128-R, M24C64-R, M24C32-R series
Customer(s):	All customers
Type of the change:	Maximum Write time
Reason for the change:	Improve product feature
Description of the change:	Write time max changes from 10ms to 5ms
Forecasted date of the change:	Week 27 2007
Forecasted availability date of qualification sample for the customer(s):	Week 27 2007
Forecasted date for the internal STMicroelectronics change, Qualification report availability:	Week 27 2007
Marking to identify the changed product:	Change effective from Date code 7 27 onwards
Description of the qualification program:	Validation tests
Product Line(s) and/or Part Number(s):	128Kbit, 64Kbit, 32Kbit I ² C EEPROM, 1.8V series (See list in appendix 2)
Manufacturing location:	ST AMK & Chartered (Singapore) wafer diffusion plants
Estimated date of first shipment:	End of July 2007
Division Product Manager: B. RODRIGUES	Date:
Group QA Manager: N. YACKOWLEW	Date:

Appendix 2 – Concerned products

Commercial sales types
M24C32-RDW6TP
M24C32-RMB6TG
M24C32-RMN6TP
M24C64-RDW6TP
M24C64-RMN6TP
M24128-BRDW6TP
M24128-BRMN6TP
M24128-RPW21/90



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Document Revision History

Date	Rev.	Description of the Revision
Apr. 10, 2007	1.00	First Draft creation (Christian POLI)

Source Documents & Reference Documents

Source document Title	Rev.:	Date:

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