



Product/Process Change Notice - PCN 23_0141 Rev. -

Analog Devices, Inc. One Analog Way, Wilmington, MA 01887, USA

This notice is to inform you of a change that will be made to certain ADI products (see Appendix A) that you may have purchased in the last 2 years. **Any inquiries or requests with this PCN (additional data or samples) must be sent to ADI within 30 days of publication date.** ADI contact information is listed below.

PCN Title:	LTM4628 Data Sheet Revision
Publication Date:	07-Aug-2023
Effectivity Date:	07-Aug-2023 <i>(the earliest date that a customer could expect to receive changed material)</i>
Revision Description:	Initial Release.

Description Of Change:

FSYNC changed from 400kHz-780kHz to 400kHz-750kHz.

Reason For Change:

The data sheet is being updated to accurately reflect device capabilities.

Impact of the change (positive or negative) on fit, form, function & reliability:

This change has no impact to form, fit, function, quality, or reliability. This is a data sheet change only with no change to product.

Summary of Supporting Information:

Changes are reflected in Product Data Sheet revision F.

Supporting Documents

Attachment 1: Type: Datasheet Specification Comparison

[ADI_PCN_23_0141_Rev_-_LTM4628PCN\(max_frequencyto750\).pdf...](#)

Note: If applicable, the device material declaration will be updated due to material change.

ADI Contact Information:

For questions on this PCN, please send an email to the regional contacts below or contact your local ADI sales representatives.

Americas:	Europe:	Japan:	Rest of Asia:
PCN_Americas@analog.com	PCN_Europe@analog.com	PCN_Japan@analog.com	PCN_ROA@analog.com

Appendix A - Affected ADI Models:

Added Parts On This Revision - Product Family / Model Number (7)

LTM4628 / LTM4628EV#2HUPBF

LTM4628 / LTM4628EV#PBF

LTM4628 / LTM4628EY#PBF

LTM4628 / LTM4628IV#PBF

LTM4628 / LTM4628IY

LTM4628 / LTM4628IY#PBF

LTM4628 / LTM4628IY#PBF-ES3

Appendix B - Revision History:

Rev	Publish Date	Effectivity Date	Rev Description
Rev. -	07-Aug-2023	07-Aug-2023	Initial Release.

ELECTRICAL CHARACTERISTICS

The ● denotes the specifications which apply over the full internal operating temperature range (Note 2). Specified as each individual output channel. $T_A = 25^\circ\text{C}$, $V_{IN} = 12\text{V}$ and V_{RUN1} , V_{RUN2} at 5V unless otherwise noted, per the typical application in Figure 28.

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS	
V_{IN}	Input DC Voltage		●	4.5	26.5	V	
V_{OUT}	Output Voltage		●	0.6	5.5	V	
$V_{OUT1(DC)}$, $V_{OUT2(DC)}$	Output Voltage, Total Variation with Line and Load	$C_{IN} = 22\mu\text{F} \times 3$, $C_{OUT} = 100\mu\text{F} \times 1$ Ceramic, 470 μF POSCAP, MODE_PLLIN = GND, RFB1, RFB2 = 40.2k, $V_{IN} = 4.5\text{V}$ to 26.5V, $I_{OUT} = 0\text{A}$ to 8A	●	1.477	1.5	1.523	V

Input Specifications

V_{RUN1} , V_{RUN2}	RUN Pin On/Off Threshold	RUN Rising		1.1	1.25	1.40	V
$V_{RUN1HYS}$, $V_{RUN2HYS}$	RUN Pin On Hysteresis			150			mV
$I_{INRUSH(VIN)}$	Input Inrush Current at Start-Up	$I_{OUT} = 0\text{A}$, $C_{IN} = 22\mu\text{F} \times 3$, $C_{OUT} = 100\mu\text{F}$, 470 μF POSCAP $V_{OUT1} = 1.5\text{V}$, $V_{OUT2} = 1.5\text{V}$, $V_{IN} = 12\text{V}$, TRACK = 0.01 μF		1			A
$I_{Q(VIN)}$	Input Supply Bias Current	$V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$, Burst Mode Operation $V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$, Pulse-Skipping Mode $V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$, Switching Continuous Shutdown, RUN = 0, $V_{IN} = 12\text{V}$			5 15 65 60		mA mA mA μA
$I_{S(VIN)}$	Input Supply Current	$V_{IN} = 4.75\text{V}$, $V_{OUT} = 1.5\text{V}$, $I_{OUT} = 8\text{A}$ $V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$, $I_{OUT} = 8\text{A}$ $V_{IN} = 26.5\text{V}$, $V_{OUT} = 1.5\text{V}$, $I_{OUT} = 8\text{A}$			2.9 1.18 0.575		A A A

Output Specifications

$I_{OUT1(DC)}$, $I_{OUT2(DC)}$	Output Continuous Current Range	$V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$ (Note 7)		0		8	A
$\frac{\Delta V_{OUT1(LINE)}}{V_{OUT1}}$ / $\frac{\Delta V_{OUT2(LINE)}}{V_{OUT2}}$	Line Regulation Accuracy	$V_{OUT} = 1.5\text{V}$, V_{IN} from 4.5V to 26.5V $I_{OUT} = 0\text{A}$ for Each Output,	●		0.010	0.04	%/V
$\frac{\Delta V_{OUT1(LOAD)}}{V_{OUT1}}$ / $\frac{\Delta V_{OUT2(LOAD)}}{V_{OUT2}}$	Load Regulation Accuracy	For Each Output, $V_{OUT} = 1.5\text{V}$, 0A to 8A $V_{IN} = 12\text{V}$ (Note 7)	●		0.15	0.3	%
$V_{OUT1(AC)}$, $V_{OUT2(AC)}$	Output Ripple Voltage	$I_{OUT} = 0\text{A}$, $C_{OUT} = 100\mu\text{F}$ X5R Ceramic, 470 μF POSCAP $V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$			15		mV _{p-p}
f_S (Each Channel)	Output Ripple Voltage Frequency	$V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$, $f_{SET} = 2.5\text{V}$ (Note 4)			780		kHz
f_{SYNC} (Each Channel)	SYNC Capture Range			400		750	kHz
$\Delta V_{OUTSTART}$ (Each Channel)	Turn-On Overshoot	$C_{OUT} = 100\mu\text{F}$ X5R Ceramic, 470 μF POSCAP, $V_{OUT} = 1.5\text{V}$, $I_{OUT} = 0\text{A}$ $V_{IN} = 12\text{V}$			10		mV
t_{START} (Each Channel)	Turn-On Time	$C_{OUT} = 100\mu\text{F}$ X5R Ceramic, 470 μF POSCAP, No Load, TRACK/SS with 0.01 μF to GND, $V_{IN} = 12\text{V}$			5		ms
$\Delta V_{OUT(LS)}$ (Each Channel)	Peak Deviation for Dynamic Load	Load: 0% to 50% to 0% of Full Load $C_{OUT} = 22\mu\text{F} \times 3$ X5R Ceramic, 470 μF POSCAP $V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$			30		mV
t_{SETTLE} (Each Channel)	Settling Time for Dynamic Load Step	Load: 0% to 50% to 0% of Full Load, $V_{IN} = 12\text{V}$, $C_{OUT} = 100\mu\text{F}$, $C_{OUT} = 470\mu\text{F}$			20		μs
$I_{OUT(PK)}$ (Each Channel)	Output Current Limit	$V_{IN} = 12\text{V}$, $V_{OUT} = 1.5\text{V}$			15		A

Control Section

V_{FB1} , V_{FB2}	Voltage at V_{FB} Pins	$I_{OUT} = 0\text{A}$, $V_{OUT} = 1.5\text{V}$	●	0.592	0.600	0.606	V
I_{FB1} , I_{FB2}	Leakage Current of V_{FB1} , V_{FB2}	(Note 6)			-5	-20	nA
V_{OVL}	Feedback Overvoltage Lockout		●	0.64	0.66	0.68	V

4628fe