


**PRODUCT / PROCESS CHANGE NOTIFICATION**

**1. PCN basic data**

<b>1.1 Company</b>		STMicroelectronics International N.V
<b>1.2 PCN No.</b>	MDG/24/14486	
<b>1.3 Title of PCN</b>	STM32U575x & STM32U585x - product Enhancement	
<b>1.4 Product Category</b>	STM32U575x STM32U585x	
<b>1.5 Issue date</b>	2024-07-10	

**2. PCN Team**

<b>2.1 Contact supplier</b>	
<b>2.1.1 Name</b>	PIKE EMMA
<b>2.1.2 Phone</b>	+44 1628896111
<b>2.1.3 Email</b>	emma.pike@st.com
<b>2.2 Change responsibility</b>	
<b>2.2.1 Product Manager</b>	Ricardo Antonio DE SA EARP
<b>2.1.2 Marketing Manager</b>	Veronique BARLATIER
<b>2.1.3 Quality Manager</b>	Pascal NARCHE

**3. Change**

<b>3.1 Category</b>	<b>3.2 Type of change</b>	<b>3.3 Manufacturing Location</b>
General Product & Design	Die redesign : Mask or mask set change with new die design like metallization (specifically chip frontside) or bug fix	TSMC FAB14 (Taiwan)

**4. Description of change**

	Old	New
<b>4.1 Description</b>	STM32U575xx and STM32U585xx - (Die482 - cut3.1 revision W) product shows several limitations as described in Errata Sheet (ES0499 - Rev 9 -January 2024) : - Device hang-up can occur when wakeup request received a few clock cycles before entering Stop 2 or Stop 3 mode - Device may be locked upon system reset under Stop 2 mode - Device may hang up when entering low-power modes	STM32U575xx and STM32U585xx - (Die482 - cut3.3 revision U) product enhancement fixes three limitations related to low-power modes as described in Errata Sheet (ES0499 - Rev 10 - July 2024): - Device hang-up can occur when wakeup request received a few clock cycles before entering Stop 2 or Stop 3 mode - Device may be locked upon system reset under Stop 2 mode - Device may hang up when entering low-power modes
<b>4.2 Anticipated Impact on form,fit, function, quality, reliability or processability?</b>	Functionality enhancement	

**5. Reason / motivation for change**

<b>5.1 Motivation</b>	Improvements was implemented to increase robustness, performances and quality of our products.
<b>5.2 Customer Benefit</b>	SERVICE IMPROVEMENT

**6. Marking of parts / traceability of change**

<b>6.1 Description</b>	traceability ensured by ST internal tools Die revision changes from "W" to "U" on Package Marking.
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**7. Timing / schedule**

<b>7.1 Date of qualification results</b>	2024-07-31
<b>7.2 Intended start of delivery</b>	2024-10-01
<b>7.3 Qualification sample available?</b>	Upon Request

8. Qualification / Validation			
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<b>8.1 Description</b>	14486 RERMCD1919 STM32U5 Die 482U cut3.3 - qual plan.pdf		
<b>8.2 Qualification report and qualification results</b>	Available (see attachment)	<b>Issue Date</b>	2024-07-10

9. Attachments (additional documentations)			
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14486 Public product.pdf  
 14486 RERMCD1919 STM32U5 Die 482U cut3.3 - qual plan.pdf  
 14486 PCN14486\_Additional information.pdf

10. Affected parts		
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10. 1 Current		10.2 New (if applicable)
10.1.1 Customer Part No	10.1.2 Supplier Part No	10.1.2 Supplier Part No
	STM32U575AGI6	
	STM32U575AGI6Q	
	STM32U575AII6	
	STM32U575AII6Q	
	STM32U575CGU3	
	STM32U575CIT6	
	STM32U575CIU6Q	
	STM32U575OIY6QTR	
	STM32U575QII6	
	STM32U575QII6Q	
	STM32U575RIT6	
	STM32U575VIT6	
	STM32U575VIT6Q	
	STM32U575ZIT6	
	STM32U575ZIT6Q	
	STM32U585AII6	
	STM32U585AII6Q	
	STM32U585CIU6	
	STM32U585QII6	
	STM32U585QII6Q	
	STM32U585VIT6	
	STM32U585VIT6Q	
	STM32U585ZIT6	
	STM32U585ZIT6Q	
	STM32U585RIT3	

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