

Preliminary Specification of CHIP MULTILAYER ANTENNA

Preliminary SPEC No. : NAN68-PA0004B

Part Number : LDA312G4413H-280

 Written by
 S. Ito

 Checked by
 T. Isa

 Date
 20/Dec./2007

A>Rev. by N.S. on 10/Jan./'08, B>Rev. by T.M. on 28/Feb./'08

Please refer to the latest specification before using this product since the preliminary specification could be revised without notice.

1. Electrical Characteristics

Operation Temp	-40 to +85 degree C
Polarization	Linear
Frequency (Fo)	2442MHz.
Band Width (BW)	Fo +/- 42MHz.
V.S.W.R at BW	4.0 max.
Impedance	50 Ω

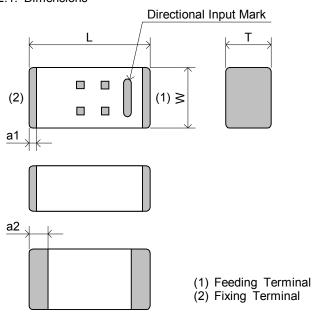
Note 1: This electrical characteristics are influenced by the land pattern, PWB size, case and so on.

Note 2: This electrical characteristics are guaranteed with our standard measurement conditon

which is used our standard land pattern and PWB in FIGURE 2.2 and 2.3.

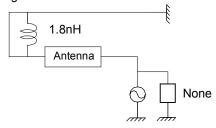
2. Mechanical

2.1. Dimensions



Mark	Dimension [mm]
L	3.2 +/-0.2
W	1.6 +/-0.2
T	1.2 +0.1/-0.2
a1	0.2 +/-0.2
a2	0.5 +/-0.2

Matching Circuit





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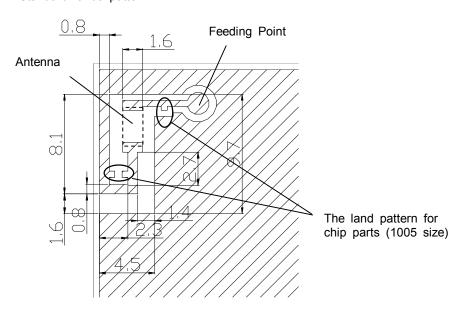
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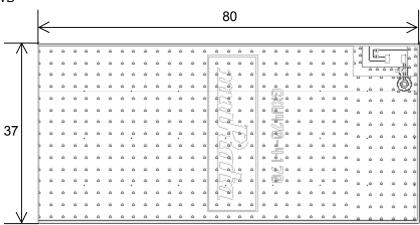
2.2. Standard lands pattern



OTHERWISESPECIFIED: +/-0.1

UNIT: mm

2.3. Standard PWB



TOLERANCES UNLESS OTHERWISE SPECIFIED: +/-1.0 UNIT: mm

Note: Impedance of signal lines should be 50 ohms including land pattern. The standard condition is applying the glass epoxy board (t = 1.0 mm, dielectric constant = 4.8, copper plating on both surfaces) and the land patterns are connected to 50 ohms micro-strip lines on backside surface through the via hole.